

Final Project 2016 > PLC\_1 [CPU 1214C DC/DC/DC] > Program blocks > Main [OB1]

Block interface

Block title: "Main Program Sweep (Cycle)"

Comment

Network 1:

Comment

```
graph LR
    I02["%I 0.2  
Start PB"] --- I01["%I 0.1  
FEB"]
    I01 --- Q00["%Q 0.0  
FAC"]
    Q00 --- End[" "]
    Q00_parallel["%Q 0.0  
FAC"] --- End
```

Network 2:

Comment

```
graph LR
    Q00["%Q 0.0  
FAC"] --- I03["%I 0.3  
FRSW / WF"]
    I03 --- I04["%I 0.4  
FRSW / WF"]
    I04 --- Q01["%Q 0.1  
DWF1"]
```

Network 3:

Comment

```
graph LR
    Q00["%Q 0.0  
FAC"] --- I05["%I 0.5  
FWS / WF"]
    I05 --- Q02["%Q 0.2  
FWS / WF"]
    Q02 --- Q03["%Q 0.3  
FRWS"]
```

100%

Main [OB1]

Properties Info Diagnostics

General

General

Information

Time stamps

Compilation

Protection

Attributes

General

Name: Main

Constant name: OB\_Main

Type: OB

Event class: Program cycle

Language: LAO

Number: 1

manual

automatic

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PLC programming

Project tree

Final Project 2016

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      - Main [OB1]
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    - Program info
    - Device proxy data
    - Text lists
    - Local modules
  - Common data
  - Documentation settings
  - Languages & resources
- Online access
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  - COM (RS232/PIR multi-master cable)
  - Intel(R) 82579LM Gigabit Network Connection
  - PC Adapter (VFP)
  - PC Internal (Local)
  - PLCSIM (PROFIBUS)
  - USB (S7USB)
  - TeleService (Automatic protocol detection)
  - Card Reader/USB memory

Final Project 2016 - PLC\_1 [CPU 1214C DC00DC] - Program blocks - Main [OB1]

Block interface

Network 3:

Comment

```
graph LR; Q0_0["%Q0.0 'PAC'"] --- Q5["%Q5 'FWS / WF'"]; Q5 --- Q2["%Q2 'FWS / WF'"]; Q2 --- Q3["%Q3 'FWS'"]; Q3 --- Q4_5_6["%Q4 'FCS1'"]; Q4_5_6 --- Q4["%Q4 'FCS1'"]; Q4_5_6 --- Q5["%Q5 'FCS2'"]; Q4_5_6 --- Q6["%Q6 'FCS3'"];
```

Network 4:

Comment

```
graph LR; Q3["%Q3 'FWS'"] --- Q7["%Q7 'FWS'"]; Q7 --- Q1_0["%Q1.0 'FWS1 NT'"]; Q1_0 --- Q1_0["%Q1.0 'FWS1 NT'"];
```

100%

Project: Edit View Insert Online Options Tools Window Help

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Project tree: Final Project 2016 -> PLC\_1 [CPU 1214C-2 DP] -> Program blocks -> Main [OB1]

Devices: Final Project 2016 -> Add new device -> Devices & networks -> PLC\_1 [CPU 1214C-2 DP] -> Device configuration -> Online & diagnostics -> Program blocks -> Add new block -> Main [OB1]

- Technology objects
- External source files
- PLC tags
- PLC data types
- Watch and force tables
- Online backups
- Program info
- Device proxy data
- Text lists
- Local modules

Common data

Documentation settings

Languages & resources

Online access

- Display/hide interfaces
- COM [RS 232/PP1 multi-master cable]
- Intel(R) 82579LM Gigabit Network Connection
- PC Adapter [MPI]
- PC internal [Local]
- PLCSIM [Pilot]
- Use [s7use]
- TeleService [Automatic protocol detection]
- Card Reader/USB memory

Block interface

Network 4:

Comment:

```
graph LR; Q03["%Q0.3 'FMS'"] --- Q07["%Q0.7 'HAB'"]; Q03 --- Q10["%Q1.0 'FWAMP'"]; Q03 --- Q11["%Q1.1 'FWAMP ST'"]
```

Network 5:

Comment:

```
graph LR; Q16["%Q1.6 'OK'"] --- Q12["%Q1.2 'BOILER On Line'"]; Q16 --- Q13["%Q1.3 'PCS OK'"]; Q16 --- Q14["%Q1.4 'PCS'"]; Q16 --- Q14["%Q1.4 'PCS'"]
```

100%

Project: Edit View Insert Online Options Tools Window Help

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Final Project 2016 - PLC\_1 [CPU 1214C-0000000] - Program blocks - Main [OB1]

PLC programming

Project tree

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      - Text lists
      - Local modules
    - Common data
    - Documentation settings
    - Languages & resources
  - Online access
    - Display/hide interfaces
      - COM [RS 232/PP1 multi-master cable]
      - Intel(R) 82579LM Gigabit Network Connection
      - PC Adapter [MP]
      - PC internal [Local]
      - PLCSIM [PN/ie]
      - Use [srtuse]
      - TeleService [Automatic protocol detection]
    - Card Reader/USB memory

Network 5:

Comment

Network 6:

Comment

PLC programming

Project tree: Final Project 2016


- Final Project 2016
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        - Main [OB1]
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      - Device proxy data
      - Test lists
      - Local modules
    - Common data
    - Documentation settings
    - Languages & resources
  - Online access
    - Display/hide interfaces
      - COM [RS232CP multi-master cable]
      - Intel(R) 82579LM Gigabit Network Connection
      - PC Adapter [MPI]
      - PC internal [Local]
      - PLCSIM [PHAS]
      - USB [S7USB]
      - TeleService [Automatic protocol detection]
      - Card Reader/USB memory

Final Project 2016 - PLC\_1 [CPU 1214C DC00DC] - Program blocks - Main [OB1]

Block interface

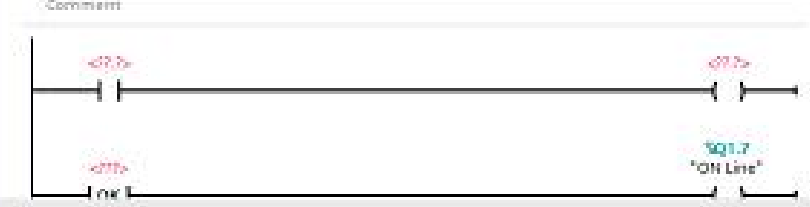
Network 6:

Comment



Network 7:

Comment



100%

Devices

- Final Project 2016
  - Add new device
  - Devices & networks
    - PLC\_1 [CPU 1214C D0D0DC]
      - Device configuration
      - Online & diagnostics
      - Program blocks
        - Add new block
        - Main [OB1]**
      - Technology objects
      - External source files
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- Online access
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    - COM (RS232/PP1 multi-meser cable)
    - Inet(R) B2579LM Gigabit Network Connection
    - PC Adapter (MPI)
    - PC internal (Local)
    - PLCSIM (Pi/H)
    - USB (ST/USB)
    - TeleService (Automatic protocol detection)
- Card Reader/USB memory

Details view

Name	Address

Block Structure  
 Step Ladder  
 Main [OB1]

Comment

